100mA / 50V Digital transistors (with built-in resistor)

DTC115GUA/DTC115GKA

Applications

Inverter, Interface, Driver

Features

- The built-in bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input, and parasitic effects are almost completely eliminated.
- 2) Only the on / off conditions need to be set for operation, making the device design easy.
- 3) Higher mounting densities can be achieved.

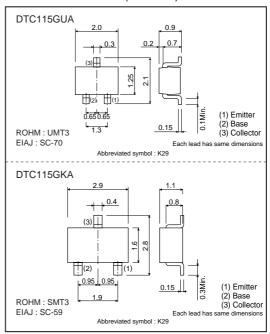
Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

Packaging specifications

	Package	UMT3	SMT3	
	Packaging type	Taping	Taping	
	Code	T106	T146	
Part No.	Basic ordering unit (pieces)	3000	3000	
DTC115GUA		0	-	
DTC115GKA		-	0	

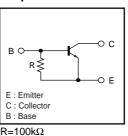
●External dimensions (Unit : mm)



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	50	V
Collector-emitter voltag	Vceo	50	V
Emitter-base voltage	VEBO	5	V
Collector current	Ic	100	mA
Collector power dissipation	Pc	200	mW
Junction temperature	Tj 150 °C		
Storage temperature	Tstg	-55 to +150	°C

●Equivalent circuit



●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	_	-	V	Ic=50μA
Collector-emitter breakdown voltag	BVceo	50	-	-	V	Ic=1mA
Emitter-base breakdown voltage	ВУЕВО	5	_	-	V	Iε=72μA
Collector cutoff current	Ісво	-	_	0.5	μΑ	Vcb=50V
Emitter cutoff current	ІЕВО	30	_	58	μΑ	V _{EB} =4V
Collector-emitter saturation voltage	VCE(sat)	-	-	0.3	V	Ic=5mA, Iв=0.25mA
DC current transfer ratio	hfe	82	_	-	-	Ic=5mA, VcE=5V
Emitter-base resistance	R	70	100	130	kΩ	-
Transition frequency	f⊤ *	_	250	_	MHz	VcE=10V, IE=-5mA, f=100MH

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^{*} Characteristics of built-in transistor

•Electrical characteristics curves

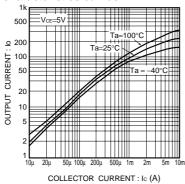


Fig.1 DC current transfer ratio vs. collector current characteristics

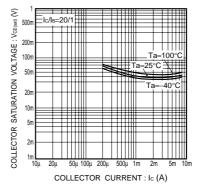


Fig.2 Collector-emitter saturation voltage vs. collector current characteristics

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